

Experiences

MaCo-Engineering is experienced in the following fields:

- Ethernet Applications
 - 10/100/1000 MBit Ethernet
 - MII, RMII, GMII, SGMII Interfaces
 - Ethernet MAC Cores
 - Data Transmission
 - Audio/Video Transmission
 - WebServer Application
 - Industrial Applications
- Bus Interfaces
 - PCI, ATA, Cardbus, LPC, PCIe
 - I2C, SPI, JTAG, SPORT
 - UART 16550, CAN
 - LVDS, Infrared, ARINC
- Audio-Processing
 - I2S, AES, AC97, SPDIF
 - Filtering, Multiplexing
 - NoiseGate, Gain
- Graphic-Video-Processing
 - VGA, DVI, HDMI, LVDS, BT656 Interfaces
 - OLED- & Display Controller
 - ArtNet DMX
 - Image OSD, Overlay, Rotation, Scaling, Clipping, 3D
- Embedded CPUs
 - Nios Software
 - SoC Software

Contact



MaCo-Engineering

**Design Services
for
FPGA
Development**

MaCo-Engineering

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MaCo-Engineering

**Design Services
for
FPGA
Development**

MaCo-Engineering offers you engineering-services around ALTERA® FPGAs from consulting to the implementation of complete designs.

The services contain realisation concepts, implementation, verification, documentation and consulting concerning new customer applications as well as conversion, optimisation and adaptation of existing customer applications.

Your desired application will be designed according to your requirements in an FPGA. **MaCo-Engineering** develops according to your ideas, requests and specifications to implement your application. You will be delivered a verified and documented ALTERA® FPGA design.

Do you already have a finished design and want to convert it from an ASIC or older PLD design to an ALTERA® FPGA design? **MaCo-Engineering** can support you!

MaCo-Engineering can also optimize existing ALTERA® FPGA designs or adapt existing designs to new requirements!

The complete hardware development around your FPGA can be done by **MaCo-Engineering** as well.

MaCo-Engineering offers you the following engineering services:

- Design of customer applications into ALTERA® FPGAs
 - Specification
 - Implementation
 - Verification
 - Simulation
 - Documentation
- Customisation of existing designs
 - Converting into ALTERA® FPGAs
 - Optimization of FPGA designs
 - Adaptation of FPGA designs to new requirements
- Consulting services around ALTERA® FPGAs
- Hardware Development

- Design Languages
 - VHDL
 - AHDL
 - graphical entry
- Software-Tools
 - ALTERA® Software Suite
 - Quartus® II
 - Nios® IDE, SBT
 - SOPC Builder, QSYS
 - Mentor Graphics
 - ModelSim
 - Altium
 - Altium Designer
- FPGA-Experiences
 - Altera® FPGAs
 - all older Altera FPGAs
 - MAX® Series
 - Cyclone™ Series
 - Arria® Series
 - Stratix® Series
- Embedded-Processor Experiences
 - Nios® Family
 - SoC Family